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Substitute for form 1449 PTO

DEC 14 2005
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Sheet 1 of 4

Complete if Known

Application Number	10/796,398
Filing Date	03/08/2004
First Named Inventor	W. McKinzie
Art Unit	2817
Examiner Name	Seungsook Ham
Attorney Docket Number	42372-0004

U. S. PATENT DOCUMENTS

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A	US- 6,538,538	03-26-2003	Hreish et al.	
MM	B	US- 6,075,485	06-13-2000	Lilly et al.	
MM	C	US- 6,061,025	05-09-2000	Jackson et al.	
MM	D	US- 6,262,495	07-17-2001	Yablonovitch et al.	
MM	E	US- 6,411,261	06-25-2002	Lilly	
MM	F	US- 6,476,771	11-05-2002	McKinzie, III	
MM	G	US- 6,501,427	12-31-2002	Lilly et al.	
MM	H	US- 6,512,494	01-28-2003	Diaz et al.	
MM	I	US- 6,525,695	02-25-2003	McKinzie, III	
MM	J	US- 6,542,342	04-01-2003	Hayashi et al.	
MM	K	US- 6,567,048	05-20-2003	McKinzie et al.	
MM	L	US- 6,590,531	07-08-2003	McKinzie, III et al.	
MM	M	US- 6,670,932	12-30-2003	Diaz et al.	
MM	N	US- 6,646,605	11-11-2003	McKinzie, III et al.	
MM	O	US- 6,690,327	02-10-2004	McKinzie, III et al.	
MM	P	US- 6,768,476	07-27-2004	Lilly et al.	
MM	Q	US- 6,774,866	08-10-2004	McKinzie, III et al.	
MM	R	US- 6,774,867	08-10-2004	McKinzie, III et al.	
MM	S	US- 6,831,602	12-14-2004	McKinzie, III et al.	

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	2/21/06
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Sheet

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of 4

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M	T	US- 6,867,746	03-15-2005	Mendolia et al.	
M	U	US- 6,888,316	05-03-2005	Seo et al.	
M	V	US- 6,897,831	05-24-2005	McKinzie, III et al.	
M	W	US- 6,906,674	06-14-2005	McKinzie, III et al.	
M	X	US- 6,917,343	07-12-2005	Sanchez et al.	
M	Y	US- 6,933,895	08-23-2005	Mendolia et al.	
M	Z	US- 6,937,192	08-30-2005	Mendolia et al.	
M	AA	US- 2002/0183013 A1	12-05-2002	Auckland et al.	
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M	AD	US- 2003/0071763 A1	04-17-2003	McKinzie, III et al.	
M	AE	US- 2003/0137457 A1	07-24-2003	McKinzie, III et al.	
M	AF	US- 2005/0029632 A1	02-10-2005	McKinzie, III et al.	
M	AG	US- 5,973,929	10-26-1999	Arakawa et al.	
M	AH	US- 5,451,917	09-19-1995	Yamamoto et al.	
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ "Number" ⁴ "Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶

Examiner Signature

Date Considered

2/24/06

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		Application Number	10/796,398
		Filing Date	03/08/2004
		First Named Inventor	W. McKinzie
		Art Unit	2817
		Examiner Name	Seungsook Ham
Sheet	3	of	4
		Attorney Docket Number	42372-0004

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
<i>AM</i>	AI	International Search Report dated April 19, 2005, for International Application No. PCT/US05/13235.		
<i>AM</i>	AJ	International Search Report dated October 21, 2005, for International Application No. PCT/US05/06784.		
<i>AM</i>	AK	Abhari et al., "Suppression of Parallel-Plate Noise in High Speed Circuits Using a Metallic Electromagnetic Band-Gap Structure", 2002 IEEE MTT-S Intl. Microwave Symp. Digest,		
		pp. 493-496, Seattle, WA, June 2-7, 2002.		
<i>AM</i>	AL	Daniel R. Sievenpiper, "High-Impedance Electromagnetic Surfaces," dissertation, UCLA Electrical Engineering Dept., filed January 1999.		
<i>AM</i>	AM	Abhari et al., "Metallo-Dielectric Electromagnetic Bandgap Structures for Suppression and Isolation of the Parallel-Plate Noise in High Speed Circuits,"		
		IEEE Trans. MTT, Vol. 51, No. 6, June 2003.		
<i>AM</i>	AN	Kamgaing et al., "High-Impedance Electromagnetic Surfaces for Parallel-Plate Mode Suppression in High Speed Digital Systems", IEEE 11th Topical Meeting on Electrical		
		Performance of Electronic Packaging (EPEP), Monterey, CA, Oct. 21-23, 2002.		

Examiner Signature		Date Considered	2/22/06
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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				Filing Date	03/08/2004
				First Named Inventor	W. McKinzie
				Art Unit	2817
				Examiner Name	Seungsook Ham
Sheet	4	of	4	Attorney Docket Number	42372-0004

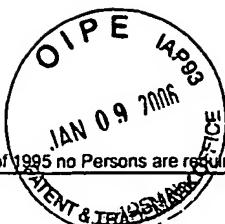
NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T ²
<i>AM</i>	AO	Kamgaing et al., "A Novel Power Plane with Integrated Simultaneous Switching Noise Mitigation Capability Using High Impedance Surface," IEEE Microwave and Wireless Components Letter, Vol. 13, No. 1, January 2003.				
<i>AM</i>	AP	Kamgaing et al., "Inductance-Enhanced High-Impedance Surfaces for Broadband Simultaneous Switching Noise Mitigation in Power Planes,"				
<i>AM</i>	AQ	2003 IEEE MTT-S Intl. Microwave Symp. Digest, pp. 2165-2168, Philadelphia, PA, June 8-13, 2003.				
<i>AM</i>	AQ	Kamgaing et al., "Development and Application fo Physics-based Compact Models for High-Impedance Electromagnetic Surfaces Integrated in a Power Plane Configuration", IEEE Intl. Antennas and Propagation Symp., Vol. 4, pp. 442-445, Columbus, OH, June 22-27, 2003.				
<i>AM</i>	AR	Lee, Thomas H., "A Vertical Leap for Microchips" Scientific America, p. 9-18, January 2002				

Examiner Signature		Date Considered	2/2/06
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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Complete if Known

Application Number	10/796,398
Filing Date	March 8, 2004
First Named Inventor	W. McKinzie
Art Unit	2817
Examiner Name	Seungsook Ham
Attorney Docket Number	42372-0004

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)				

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Date
Considered

2/22/08

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